

REMARKS.

The Examiner has rejected claims 4, 5 and 11 under 35 U.S.C. 112 as indefinite. Amendments to these claims correcting antecedent basis and defining terms within the claims consistent with the disclosure in the specification have been made. Applicant respectfully contends that these amendments overcome the Examiner's rejections.

The Examiner's identification of allowable subject matter in claims 2, 6-8 and 12-19 if amended to include the limitations of original and intervening base claims is noted with appreciation. Claim 2 has been amended to include all limitations of claim 1 from which it depended and is now believed to be in condition for allowance. In view of the argument below, claims 6 and 12 have not been so amended pending review of the arguments presented with respect to claims 1 and 10 from which they depend.

The Examiner has rejected claims 1, 3-5, 10 and 11 as anticipated by Matsuoka et al. (5809009). Claims 1 and 10 have been amended to clarify the present invention. The Examiner has cited FIG. 10 element 35 of Matsuoka as "where the quadrature signal is stored and then extracted". The applicant respectfully contends that the description in Matsuoka does not disclose a storage element wherein the quadrature data is stored and then extracted not only for the first analysis but for subsequent processing. As described in the present application on page 6 lines 16 -21, "FIG. 5 shows a diagram of the conceptual operation of the reflection buffer. In the first pilot detection position 58, the input to the correlator is derived from the demodulated data stream. In the second detection position 60, the pointer is reset and the pilot is again input to the correlator from the reflection buffer. In the third detection position 62, the pointer is again reset and the pilot and data input to the correlator is derived from the reflection buffer. The pointer to the data in the reflection buffer is altered based on the timing error computed above."

The buffer storage of the present invention allows the pilot bitstream to be compared from the same data after application of a timing correction factor. Matsuoka does not disclose or suggest such capability. Element 35 is merely the signal extraction section through which all data flows. No storage for subsequent retrieval is disclosed. "The symbol extraction section 35 receives successive I, Q sample pairs of a digital quadrature baseband signal 11, and selects those samples which most closely correspond

in timing to the contents of respective symbol periods as specified by the symbol synchronizing signal ..." Col. 17 Lines 14-18.

The applicant respectfully contends that claims 1 and 10 as amended are now allowable and that claims 4-5 and 12-19 as dependent on those claims respectively are also allowable. Claim 3 has been amended for dependency on claim 2 and is therefore believed to be allowable.

The Examiner has rejected claim 9 as obvious over Matsuoka and further in view of Shirakata et al. 6618352. The applicant respectfully traverses the rejection of claim 9 in view of the argument presented above with respect to Matsuoka. Shirakata et al similarly does not disclose the use of a storage buffer and claim 9 as dependent on claim 1 is therefore believed to be allowable.

The applicant believes that all claims now pending in the application as amended are in condition for allowance and action by the Examiner in that regard is respectfully requested.

Respectfully submitted,

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